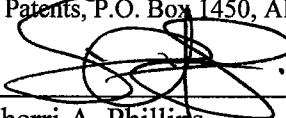


PATENT

I hereby certify that on the date specified below, this correspondence is being filed electronically via EFS-Web addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

October 30, 2008
Date


Sherri A. Phillips

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/822,275	Confirmation No. : 2177
Applicant : Jeffery W. Janzen	
Filed : April 8, 2004	Attorney Docket No.: 501286.01 (30262/US)
Art Unit : 2188	Customer No. : 27,076
Examiner : Shawn Eland	
Title : SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF COMPONENTS IN A MULTICHIP MEMORY MODULE	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. § 1.111

Sir:

Applicant acknowledges receipt of the Office Action dated June 16, 2008, and herewith petitions the Commissioner for Patents under 37 C.F.R. § 1.136(a)(3) for a two-month extension of time for filing this response from September 16, 2008 to November 16, 2008. Submitted herewith is a check in the amount of \$490.00 to cover the cost of the extension. Any deficiency or overpayment should be charged or credited to Deposit Account No. 50-1266.

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.